SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR

(Autonomous)

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)
(Accredited by NBA & Accredited by NAAC with 'A+' Grade)
(An ISO 9001:2008 Certified Institution)
Siddharth Nagar, Narayanavanam Road, PUTTUR-517 583

Examinations Branch

TIME TABLE

M.Tech I Year I Semester (R20) Regular & Supplementary Examinations March -2024

TIMINGS: 09:30 AM to 12:30 PM

Date/Day	STRUCTURAL ENGINEERING	COMPUTER SCIENCE AND ENGINEERING	THERMAL ENGINEERING	POWER ELECTRONICS	VLSI
19-03-2024 (TUESDAY)	20HS0823 Research Methodology and IPR (Common to All)				
21-03-2024 (THURSDAY)	20CE1001 Advanced Structural Analysis	20CS5001 Advanced Data Structures	20ME3101 Thermodynamics and Combustion	20EE2101 Electric Drives Systems	20EC4201 VLSI Technology
23-03-2024 (SATURDAY)	20CE1002 Advanced Solid Mechanics	20CS5002 Python Programming	20ME3102 Advanced Fluid Dynamics	20EE2102 Modelling and Analysis of Electrical Machines	20EC4202 Digital IC Design
27-03-2024 (WEDNESDAY)	20CE1010 Theory of Structural Stability	20CS5012 Software Engineering and Testing	20ME3112 Nuclear Engineering	20EE2103 Advanced Power Electronic Circuits	20EC4101 Embedded System Design
30-03-2024 (SATURDAY)	20CE1011 Structural Health Monitoring	20CS5016 Distributed Systems	20ME3115 Air Conditioning System Design	20EE2107 Static VAR Controllers and Harmonic Filtering	20EC4205 Verilog HDL

Note: If any omissions or clashes in this time table may please be informed to the undersigned immediately.

CONTROLLER OF EXAMINATIONS

PRINCIPAL